

Discussion at AIS, TU München 20 July 2017

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KIT - Die Forschungsuniversität in der Helmholtz-Gemeinschaft

www.kit.edu

Project: IMPROVE APS





Institut für Theoretische Informatik Anwendungsorientierte Formale Verifikation Prof. Dr. B. Beckert







Purpose of this meeting



Scientists

propose a novel formal verification notation and engine. Design goal: comprehensibility and usability

meet

Engineers from automation industry

- know industrial practice
- know industrial requirements for validation processes
- \Rightarrow can value usability of the proposal

Questions we would like to ask ...



- If you use test tables today:
 - What feature do you miss in your table technique?
 - Which presented generalisation feature do you like (and which not ...)?
- Would validation using generalised test tables be a valuable extension to your validation processes?
- What are the requirements to use a formal verification tool like the presented?

(e.g., tradeoff specification effort vs. more coverage)



Automated production systems must be validated



- Automated production systems must be validated
- Practice today: Test tables describe individual test cases

| Sequence of signals in clock cycle | | | | | | | | |
|------------------------------------|-----------------|-----------------|------------------|------------------|--|--|--|--|
| | In ₁ | In ₂ | Out ₁ | Out ₂ | | | | |
| 1 | 1 | 0 | FALSE | FALSE | | | | |
| 2 | 3 | 1 | FALSE | TRUE | | | | |
| 3 | 3 | 5 | FALSE | TRUE | | | | |
| 4 | 2 | 1 | TRUE | TRUE | | | | |
| 5 | 9 | 4 | TRUE | TRUE | | | | |
| 6 | 3 | 4 | TRUE | FALSE | | | | |
| 7 | 3 | 5 | TRUE | FALSE | | | | |
| 8 | 9 | 4 | TRUE | FALSE | | | | |

Commence of structure in other lands



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Goals of project:

→ Increase test coverage!



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- → Increase test coverage!
- → Keep Comprehensibility!



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Goals of project:

- → Increase test coverage!
- → Keep Comprehensibility!
- → Increase Expressiveness!



Generalise known concept: Test tables (\sim Excel sheets)



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Generalisation Concepts:



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(1) Abstraction: Not concrete values, but constraints -, [4,8], > 10 OR ≤ 2



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References:

"same value as value of X two cycles earlier"



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Generalisation Concepts:

(1) Abstraction: Not concrete values, but constraints -, [4,8], $> 10 \text{ OR} \le 2$

References:

"same value as value of X two cycles earlier"

3 Variable durations:

"must hold for 5-10 cycles", "... for at least 20 cycles"







| | Lo | Hi | Ready | ОК |
|---|----|----|-------|-------|
| 1 | 1 | 0 | FALSE | FALSE |
| 2 | 3 | 1 | FALSE | TRUE |
| 3 | 3 | 5 | FALSE | TRUE |
| 4 | 2 | 1 | TRUE | TRUE |
| 5 | 9 | 4 | TRUE | TRUE |
| 6 | 3 | 4 | TRUE | FALSE |
| 7 | 3 | 5 | TRUE | FALSE |
| 8 | 9 | 4 | TRUE | FALSE |

Concrete table - difficult to recognize concept











| | Lo | Hi | Ready | ОК | Dur |
|---|----|----|-------|----|--------|
| 1 | - | - | FALSE | - | [0,10] |





| | Lo | Hi | Ready | ОК | Dur |
|---|----|------|-------|------|---------|
| 1 | - | - | FALSE | - | [0, 10] |
| 2 | - | > Lo | TRUE | TRUE | - |





| | Lo | Hi | Ready | ОК | Dur |
|---|----|-----------|-------|-------|---------|
| 1 | - | - | FALSE | - | [0, 10] |
| 2 | - | > Lo | TRUE | TRUE | - |
| 3 | - | \leq Lo | TRUE | FALSE | 1 |





| | Lo | Hi | Ready | OK | Dur |
|---|----|-----------|-------|-------|---------|
| 1 | - | - | FALSE | - | [0, 10] |
| 2 | - | > Lo | TRUE | TRUE | - |
| 3 | - | \leq Lo | TRUE | FALSE | 1 |
| 4 | - | _ | TRUE | FALSE | - |





| | Lo | Hi | Ready | ОК | Dur |
|---|----|-----------|-------|-------|---------|
| 1 | - | - | FALSE | - | [0, 10] |
| 2 | - | > Lo | TRUE | TRUE | - |
| 3 | - | \leq Lo | TRUE | FALSE | 1 |
| 4 | - | _ | TRUE | FALSE | - |

Generalised table:

- better coverage (many test cases covered by this table)
- still comprehensible
- documents the idea of the block

Tool





http://formal.iti.kit.edu/stvs

- Concepts for Interactive Table Exploration
- "Table-driven debugging"
- Prototypical implementation is there extension on the way

Verification





Example Production System





Exploring the Counterexample



| ¥ | INPUT Weight | INPUT LightBarrier0 | INPUT LightBarrier1 | INPUT LightBarrier2 | INPUT LightBarrier3 | INPUT Workpi | MoveBelt | Push1 | Push2 | ERROR | Duration |
|---|---|---|--|--|--|---|--|---|--|--|--------------|
| 0 | [1,20] 3 | TRUE TRUE | FALSE | FALSE | FALSE | EXPRESS EXPRESS | TRUE | FALSE | FALSE | NO_ERROR NO_ERROR | 1 1 |
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Exploring the Counterexample



| ▼ Sp | • Specification Table | | | | | | | | | | | |
|------|--|---|---|---|---|---|--|---|---|--|----------|--|
| | INPUT Weight | INPUT LightBarrier0 | INPUT LightBarrier1 | INPUT LightBarrier2 | INPUT LightBarrier3 | INPUT Workpi | OUTPUT MoveBelt | OUTPUT Push1 | OUTPUT Push2 | OUTPUT ERROR | Duration | |
| 0 | [1,20] | TRUE | FALSE | FALSE | FALSE | EXPRESS EXPRESS | TRUE | FALSE | FALSE | NO_ERROR NO_ERROR | 1 | |
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| ▼ 5 | pecification Ta | ble 🗳 | | | | | | | | | |
|-----|---|---|---|---|--|---|--|---|---|--|----------|
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